

REMARKS

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Final Office Action dated January 25, 2008 has been received and its contents carefully reviewed.

By this response, claim 27 has been amended. No new matter has been added. Claims 28-32 and 34 are cancelled without prejudice or disclaimer. Thus, claims 27 and 33 are pending in the application. Reconsideration and withdrawal of the rejection is respectfully requested in view of the above amendments and the following remarks.

In the Office Action, claims 27-34 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 6,147,667 to Yamazaki et al. (hereinafter “Yamazaki”) in view of Single-Crystal Si-Films via a Low Substrate Temperature Excimer-Laser Crystallization Method by Sposili et al. (hereinafter “Sposili”).

The rejection of claims 27-34 as being unpatentable over Yamazaki in view of Sposili is respectfully traversed and reconsideration is requested. Claims 28-32 and 34 have been cancelled. Thus, the rejection of these cancelled claims is rendered moot.

Claim 27 is allowable over Yamazaki in view of Sposili in that the structure of claim 27 recites a combination of elements including, for example, “a pixel array including a switching device and a pixel electrode in the first region of the first substrate, the pixel array having an active layer formed of amorphous silicon”, “a driver circuit in the second region of the first substrate, the driver circuit having an active layer formed of polycrystalline silicon having silicon grains growing by single-pulse lateral growth process” and “a controller unit in the third region of the first substrate, wherein the pixel array, driver circuit and controller unit are integrally located on the first substrate, and wherein the controller unit includes an active layer formed of single crystalline silicon in which silicon grains have a length beyond a single-pulse lateral growth distance and is grown by single-pulse lateral growth process.” None of the cited references, singly or in combination, teaches or suggests at least these features of the claimed invention.

Yamazaki merely disclosed “the pixel matrix circuit, the driver circuit and the logic circuit includes a plurality thin film transistors having thin film of the crystallized silicon...” See claims 1-5, 7-8, 10-15, 17-18 and 20.

That is, Yamazaki does not disclose the pixel matrix including thin film transistors having thin film of the amorphous silicon. Further, Yamazaki merely disclosed that the driver circuit and the logic circuit include a plurality thin film transistors having thin film of the crystallized silicon. That is, Yamazaki fails to clearly point out that the driver circuit includes the thin film transistors having thin film of the polycrystalline silicon and the logic circuit includes the thin film transistors having thin film of the single crystal silicon. Thus, Yamazaki does not teach or suggest at least the structural features of the claimed invention.

Accordingly, Applicants respectfully request withdrawal of the rejection of claim 27.

Applicants respectfully traverse the rejection of claim 33, and reconsideration is respectfully requested. Claim 33 is allowable at least by virtue of the fact that it depends from claim 27, which Applicants have submitted above is allowable.

Applicants believe the foregoing amendments place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed.

Dated: **21 April 2008**

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